| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 760976 | (first same second same buffer$2 same (switch$2 or transistor$2)) and 365/185.11, "230.03", "63", "72".ccls. | US-PGPUB; USPAT | OR | ON | 2005/06/14 08:39 |
| L2 | 261 | (first same second same buffer$2 same (switch$2 or transistor$2)) and 365/185.11,230.03,63,72.ccls. | US-PGPUB; USPAT | OR | ON | 2005/06/14 09:59 |
| L3 | 16 | ((first adj2 buffer) same (second adj2 buffer)) and 365/185.11., 185.33.ccls. | US-PGPUB; USPAT | OR | ON | 2005/06/14 10:02 |
| L4 | 90 | ((first adj2 buffer) same (second adj2 buffer)) and 365/230.03.ccls. | US-PGPUB; USPAT | OR | ON | 2005/06/14 10:07 |
| L5 | 29 | ((first adj2 buffer) same (second adj2 buffer)) and 365/63,72.ccls. | US-PGPUB; USPAT | OR | ON | 2005/06/14 10:08 |
| L6 | 40 | ((first adj2 buffer) same (second adj2 buffer) same memory same (switch$3 or transistor$3)) | EPO; JPO | OR | ON | 2005/06/14 10:09 |

*All checked*
*Tai 5*
*6/14/05*